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Serial No. **Unknown**

7. (Amended) A control node for use in a method according to claim 1, the control node including a control processor and a signalling interface, which signalling interface, in use, communicates signals with a plurality of gateways in a circuit-switched network, the control processor being arranged to carry out the following steps in sequence:

- a) communicating instructions to the plurality of gateways to transmit polling messages to a destination address in a circuit-switched network connected to the gateways;
- b) receiving from the plurality of gateways indications of respective delays in responses to the polling messages:
- c) selecting, depending on the respective delays, one of the gateways as the end-point of a virtual circuit.

8. (Amended) A gateway for use in a method according to claim 1, the gateway including a first interface for connection to a packet-switched network, a second interface for connection to a circuit-switched network, and a control processor including a control interface arranged to communicate control signals with a control node, the control processor being arranged to carry out the following steps in sequence:

- a) in response to a control message from the control mode transmitting a polling message to a destination address in the circuit-switched network;
- b) receiving a reply from the destination address and determining the delay of the reply;
- c) communicating the reply to the control node.

WELLS

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9. A communications network including a control node according to claim 7 and a gateway comprising:

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a first interface for connection to a packet-switched network, a second interface for connection to a circuit-switched network, and a control processor including a control interface arranged to communicate control signals with a control node, the control processor being arranged to carry out the following steps in sequence:

a) in response to a control message from the control mode transmitting a polling message to a destination address in the circuit-switched network;

b) receiving a reply from the destination address and determining the delay of the reply;

c) communicating the reply to the control node.
